



The diagram illustrates a multi-channel video signal processing system. It consists of the following components and connections:

- Inputs:**
  - 61:** ENABLE\_VIDEO signal, which branches to the ENABLE input of every channel block.
  - 41<sub>1</sub> to 41<sub>n</sub>:** A series of preamplifier outputs labeled PREAMP(1) through PREAMP(n-1) and PREAMP(n). Each preamp output is connected to the RIGHT input of its corresponding channel block.
  - GND:** Ground connections are provided for the LEFT and VETO inputs of each channel block.
- Channel Blocks:**
  - There are  $n$  identical channel blocks, each receiving an ENABLE signal and a preamp signal.
  - Each block has four outputs: LEFT, VETO, CENTER, and PULSE\_OUT.
  - The LEFT and VETO outputs of all blocks are connected to a common vertical bus.
  - The PULSE\_OUT of each block is connected to a corresponding input of the OR gate (labeled 43<sub>1</sub> to 43<sub>n</sub>).
- OR Gate (71):**
  - The OR gate receives multiple inputs from the PULSE\_OUT lines of the channel blocks.
  - Its output is labeled TOTAL\_OUT.

FIG-2

